

a second substrate including a color filter layer;
a liquid crystal layer over the first substrate to apply an electric field to the liquid crystal layer parallel to surfaces of the first and second substrates; and
a shielding layer on an outer surface of the second substrate, said shielding layer for shielding an outer electric field.

30. The device according to claim 29, wherein said shielding layer includes a transparent conductive metal layer.

31. The device according to claim 30, wherein the transparent conductive metal layer includes indium tin oxide.

32. The device according to claim 29, further comprising a pair of electrodes over the first substrate.

33. The device according to claim 32, wherein the pair of electrodes are formed a parallel electric fields of the first and second substrates.

34. The device according to claim 29, further comprising:
a passivation layer on the first substrate;
a first alignment layer on the passivation layer; and
a second alignment layer on the color filter layer.

35. The device according to claim 34, wherein the first alignment layer includes a polyimide.

36. The device according to claim 34, wherein the first alignment layer includes a photo-alignment material.

37. The device according to claim 36, wherein the photo-alignment material includes a polyvinylfluorocinnamate.

38. The device according to claim 36, wherein the photo-alignment material includes polysiloxane-based materials.

39. The device according to claim 34, wherein the second alignment layer includes a polyimide.

40. The device according to claim 34, wherein the second alignment layer includes a photo-alignment material.

41. The device according to claim 40, wherein the photo-alignment material includes a polyvinylfluorocinnamate.

42. The device according to claim 40, wherein the photo-alignment material includes polysiloxane-based materials.

43. A method of fabricating a liquid crystal display device, comprising the steps of:
forming a plurality of data bus lines and a plurality of gate bus lines on a first substrate;
forming a plurality of thin film transistors at cross points of the plurality of data bus lines and
the plurality of gate bus lines;
forming a shielding layer on an outer surface of a second substrate, said shielding layer for
shielding an outer electric field;
forming a color filter layer on the second substrate; and
forming a liquid crystal layer between the first and second substrates, the liquid crystal layer
being applied an electric field parallel to surfaces of the first and second substrates.

44. The device according to claim 43, wherein said shielding layer includes a transparent
conductive metal layer.

45. The device according to claim 44, wherein the transparent conductive metal layer include
indium tin oxide.

46. The device according to claim 43, further comprising the step of forming a pair of
electrodes over the first substrate.

47. The device according to claim 46, wherein the pair of electrodes are formed a parallel
electric fields of the first and second substrates.

48. The device according to claim 43, further comprising:

a passivation layer on the first substrate;
a first alignment layer on the passivation layer; and
a second alignment layer on the color filter layer.

49. The device according to claim 48, wherein the first alignment layer includes a polyimide.

50. The device according to claim 48, wherein the first alignment layer includes a photo-alignment material.

51. The device according to claim 50, wherein the photo-alignment material includes a polyvinylfluorocinnamate.

52. The device according to claim 50, wherein the photo-alignment material includes polysiloxane-based materials.

53. The device according to claim 48, wherein the second alignment layer includes a polyimide.

54. The device according to claim 48, wherein the second alignment layer includes a photo-alignment material.

55. The device according to claim 54, wherein the photo-alignment material includes a polyvinylfluorocinnamate.

56. The device according to claim 54, wherein the photo-alignment material includes polysiloxane-based materials.--

REMARKS

By this Preliminary Amendment, Applicants canceled original claims 1-28 and added new claims 29-56.

Applicants respectfully request that the Examiner consider the above amendment upon initial consideration on the merits of the present application.

This application is a continuation of prior application 08/934,770 filed on September 22, 1997, now indicated as allowable by the U.S.P.T.O.

Should the Examiner feel that there are any issues outstanding after consideration of this response, the Examiner is invited to contact Applicants' undersigned representative to expedite the prosecution.

EXCEPT for issue fees payable under 37 C.F.R. § 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §§ 1.16 and 1.17 which may be required,